

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Tadayoshi NAKANO et al.) Group Art Unit: Unassigned
Application No.: Unassigned) Examiner: Unassigned
Filed: March 24, 2004) Confirmation No.: Unassigned
For: SEMICONDUCTOR INTEGRATED)
CIRCUIT IN WHICH VOLTAGE DOWN)
CONVERTER OUTPUT CAN BE)
OBSERVED AS DIGITAL VALUE AND)
VOLTAGE DOWN CONVERTER)
OUTPUT VOLTAGE IS ADJUSTABLE)

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

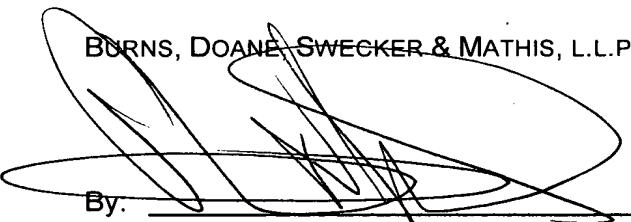
In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, the accompanying information is being submitted in accordance with 37 C.F.R. §§ 1.97 and 1.98.

Japanese Patent No. 5-325580 is discussed in the specification.

To assist the Examiner, the document is / documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

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Substitute for form 1449A/PTO & 1449B/PTO		<i>Complete if Known</i>	
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small>		Application Number	Unassigned
		Filing Date	March 24, 2004
		First Named Inventor	Tadayoshi NAKANO et al.
		Examiner Name	Unassigned
Sheet	1	of	1
		Attorney Docket Number	009683-501

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

Examiner Signature _____ **Date Considered** _____

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.